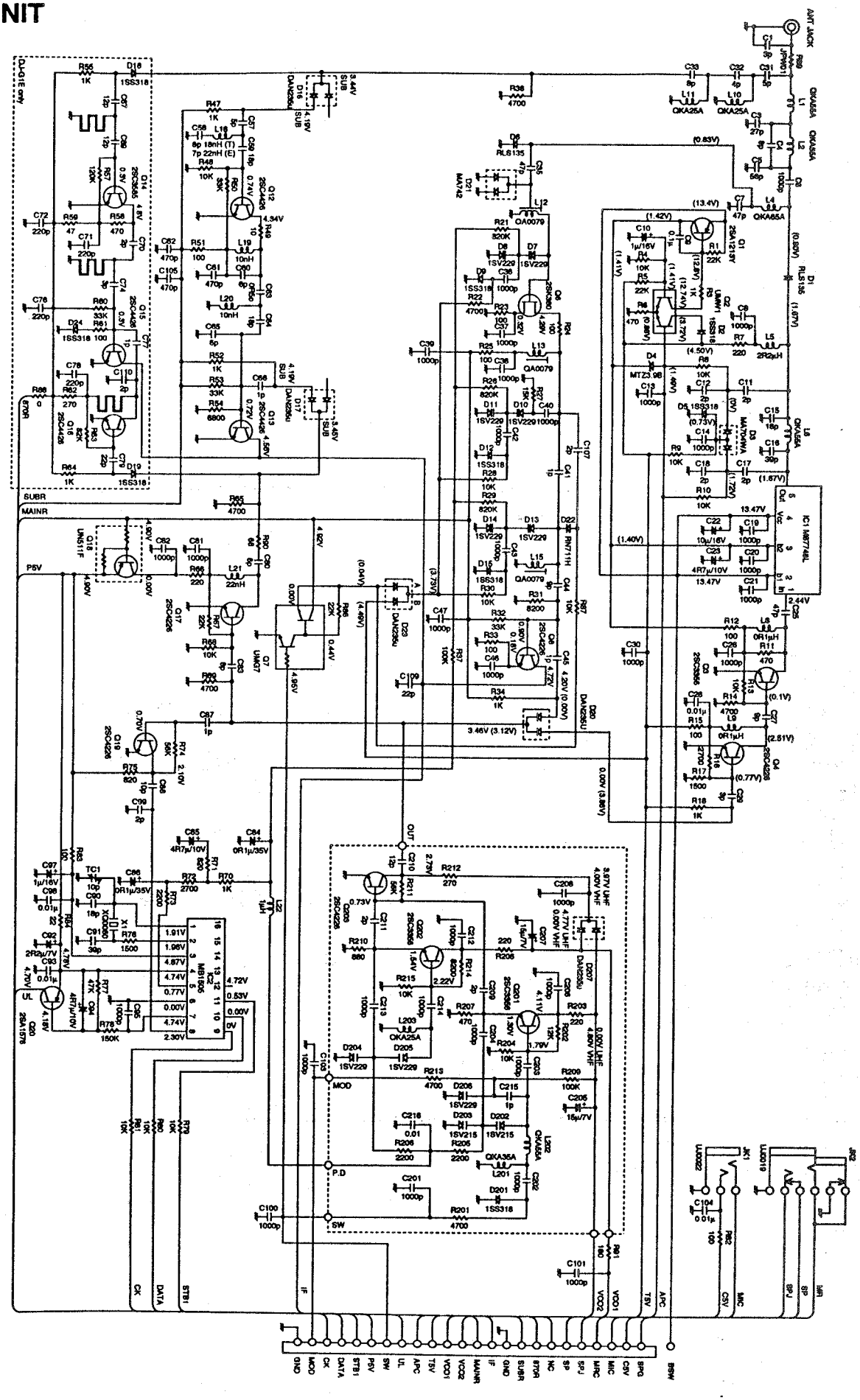
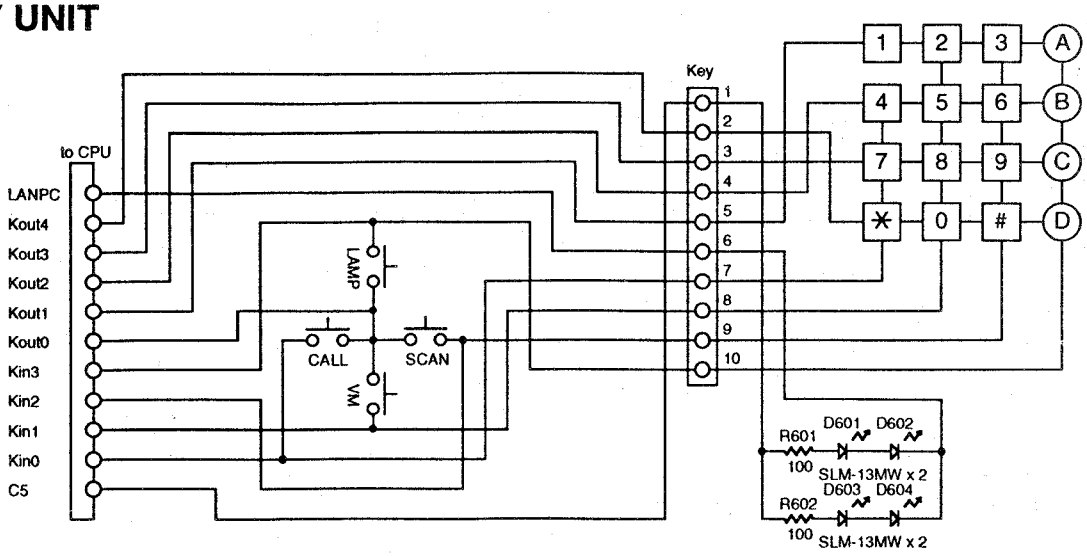
SCHEMATIC DIAGRAM

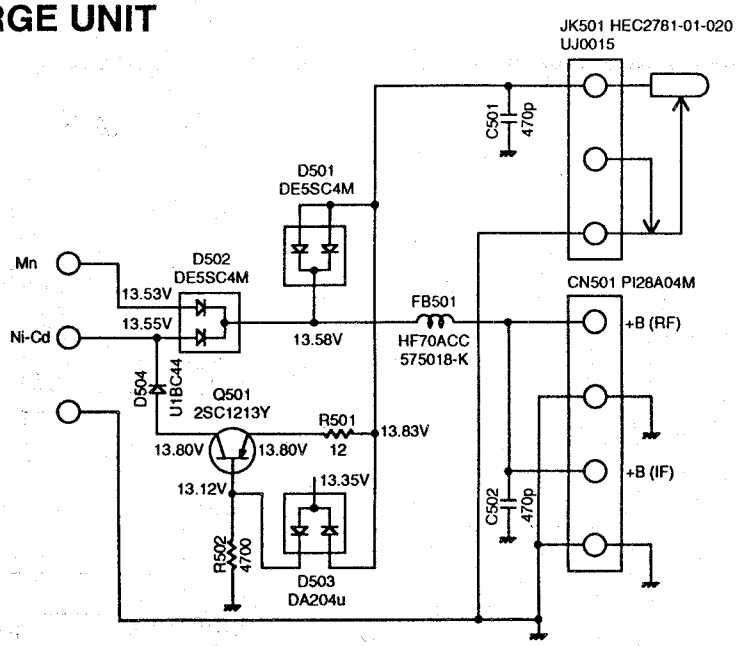
1) RF UNIT



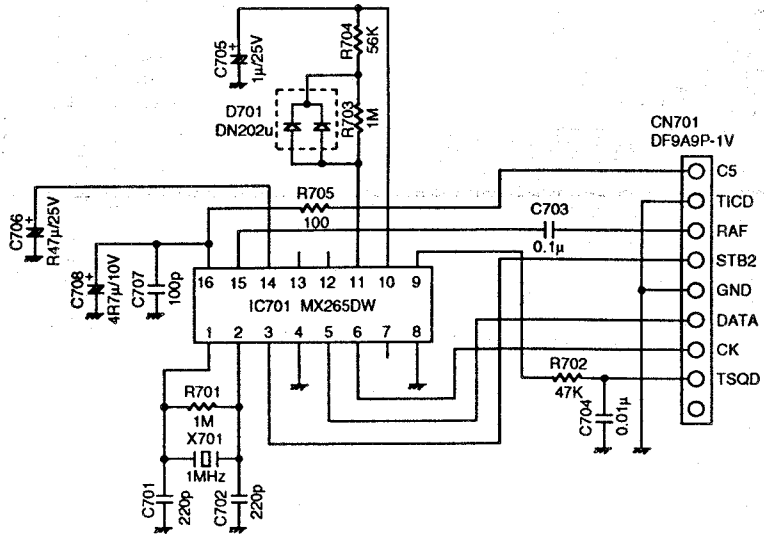
4) KEY UNIT



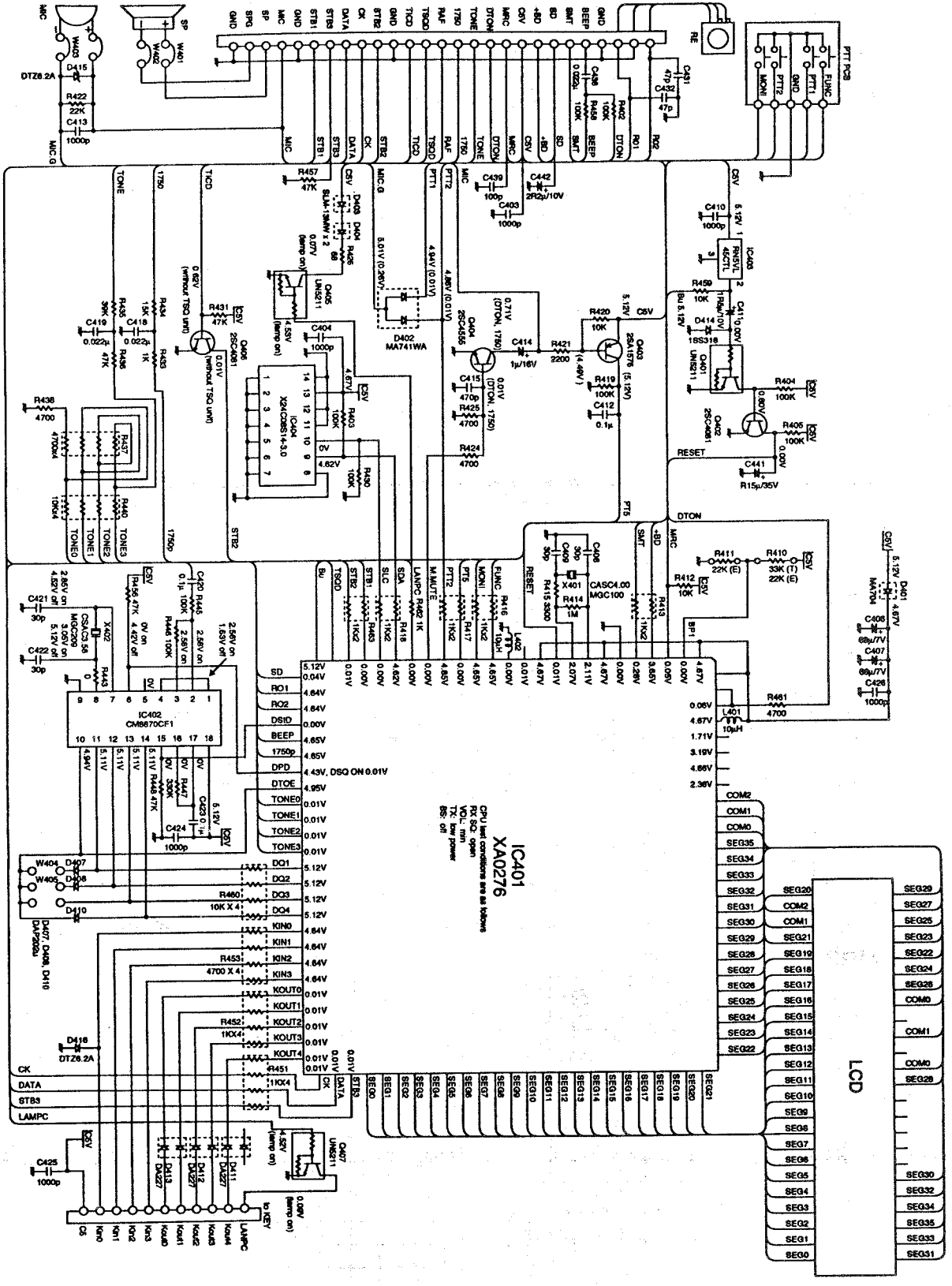
5) CHARGE UNIT



6) CTCSS Unit
EJ-16u



3) CPU Unit


IC401
XA0276
CPU load conditions are as follows
RX: SQ: open
TX: MIC: min
VOL: min
PS: low power
BS: on
LCD
IC402
CM8870CF1
IC404
X24C08S14-3.0
CASC4.00
MGC100
X401
DTOE
TONE0
TONE1
TONE2
TONE3
DPD
1750p
BEEP
DSID
RO1
RO2
SD
DQ1
DQ2
DQ3
DQ4
KIN0
KIN1
KIN2
KIN3
KOUT0
KOUT1
KOUT2
KOUT3
KOUT4
CK
DATA
STB3
LAMPC
PTT PCB
MIC
SP
RE
RESET
DTON
MRC
SMT
BEEP
TSQD
TCD
RAF
TONE
PTT1
PTT2
MONI
FUNC
GND
+BD
CSV
5.12V
4.67V
SEG0
SEG1
SEG2
SEG3
SEG4
SEG5
SEG6
SEG7
SEG8
SEG9
SEG10
SEG11
SEG12
SEG13
SEG14
SEG15
SEG16
SEG17
SEG18
SEG19
SEG20
SEG21
SEG22
SEG23
SEG24
SEG25
SEG26
SEG27
SEG28
SEG29
SEG30
SEG31
SEG32
SEG33
SEG34
SEG35
COM0
COM1
COM2

2) IF UNIT

